

Electronic Acknowledgement Receipt

EFS ID:	10967563
Application Number:	10751324
International Application Number:	
Confirmation Number:	1991
Title of Invention:	INTEGRATED CIRCUITS WITH RAM AND ROM FABRICATION OPTIONS
First Named Inventor/Applicant Name:	Raminda Udaya Madurawe
Customer Number:	33380
Filer:	Jose S. Garcia/Donna Petford
Filer Authorized By:	Jose S. Garcia
Attorney Docket Number:	
Receipt Date:	16-SEP-2011
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Application Type:	Utility under 35 USC 111(a)

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File Listing:

Document Number	Document Description	File Name	File Size(Bytes)/Message Digest	Multi Part/.zip	Pages (if appl.)
1	Power of Attorney	2651_US_POA_DRATS.pdf	1013332 22fd9cbf4954cc668c2fdfc198a01cfa8a8b9.deb	no	2

Warnings:

Information:

2	Assignee showing of ownership per 37 CFR 3.73(b).	TIER-0003_373b.pdf	430029 dea2246f668a864633219ae478b6fdfc940c 97a6	no	2
Warnings:					
Information:					
Total Files Size (in bytes):			1443361		
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